DTTF muon sorting: Wedge Sorter and Barrel Sorter

1 BS, it sorts the 4 “best” tracks out of max 24 tracks coming from the 12 WS of barrel

12 WS, each one sorts the 2 “best” tracks out of max 12 tracks coming from the 6 PHTF of a wedge
DT Wedge Sorter

Prototype fully tested @ 40 MHZ in standalone setup

Combined static test done with old version of PHTF

Combined test @ 40 MHz with new PHTF underway this week!

If test is OK, the system will be mounted on bunched test beam next October

- Tender for production ended last week - Start production by Oct 04 -
- Production done by Dec 04 (as scheduled) -
DT Barrel Sorter

Electrical scheme of main board (VME 9U, with all connectors and tranceivers) is ready

Mezzanine design under way: ghost busting and sorting 4 out 24 in 3 BX is a complex task and can fit only in a big StratixII FPGA (1508 pin)

Prototype expected by the end of November 04